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(54) Ambipolar organic transistors

(57) The present invention is related to an organic field-effect transistor comprising source and drain electrodes (1) formed separately from each other on a substrate, said substrate comprising at least an organic semiconductor layer (2) constituting a channel between said source and drain electrodes (1), an insulation layer (3) underlying said organic semiconductor layer (2) and a gate electrode (4) formed on the opposite side of the insulation layer (3), wherein said organic semiconductor layer (2) comprises holes and electrons transporters, said electron transporter comprising (6,6)-phenyl C₆₁-butyric acid methyl ester (PCBM) and wherein said hole transporters comprising poly(2-methoxy-5-(3',7'-dimethyloctyloxy)-1,4-phenylene-vinylene) (OC₁C₁₀-PPV) and/or poly(3-hexylthiophene) (P3HT).

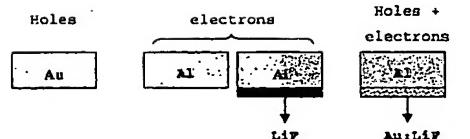
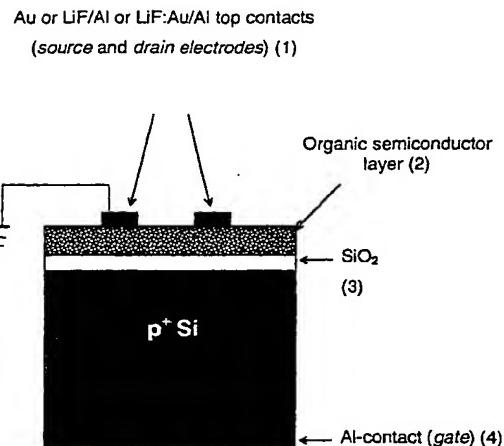


Fig. 1

Description**Field of the Invention**

[0001] The present invention is related to microelectronics and more particularly to ambipolar organic field-effect transistors comprising organic active layer material.

State of the art

[0002] Organic transistors and more particularly thin film field-effect transistors become more and more attractive in electronics because of their low-cost deposition technologies and steadily improving performance.

[0003] An organic field-effect transistor consists of materials ranging from conductors and semiconductors, to insulators.

[0004] A transistor is namely p-channel if the majority charge carriers in the channel are positive (holes), and conversely n-channel when the majority charge carriers in the channel are negative (electrons).

[0005] Complementary circuits consist of both n-channel and p-channel transistors. They are particularly useful because of advantages such as low static power dissipation and simple circuit design.

[0006] For the fabrication of organic complementary circuits the organic semiconducting p-channel and n-channel materials should exhibit relatively high and comparable mobility values for holes as well as for electrons.

[0007] Ambipolar operation has been observed in organic field-effect transistors based on ultra-pure pentacene single crystals (Schön *et al.*, Science, Vol. 287 (2000) p1022-1023).

[0008] In the single crystals, which were grown by physical vapour transport in a stream of nitrogen, accumulation (p-type) and inversion (n-type) could be established.

[0009] However, low-cost solution-processable complementary circuits require the use of two different organic materials to obtain n-channel and p-channel transistors, since defects in these materials only allow unipolar charge transport.

[0010] There are several reasons why only a limited number of high performance n-channel organic semiconductors have been discovered so far, one of them is the fact that most organic materials tend to transport holes better than electrons and a large research effort is ongoing on electron transporters. In this field the major difficulty is to find a material which is not sensitive to oxygen. Oxygen is known to be an efficient trap for electrons.

[0011] Recent developments disclose organic transistor structures, comprising a mix of two active materials which enables p-channel and n-channel operations in the same device. An example of such active materials exhibiting such properties are poly(3-hexylthiophene)

(P3HT) and N,N'-bis(2,5-di-*tert*-butylphenyl)-3,4,9,10-perylene dicarboximide (BPPC) as respectively hole and electron transporters.(Tada *et al.*, Jpn. J. Appl. Phys., Vol. 36 (1997) Pt. 2, No. 6A, L718). Currents of both polarities can be injected from the source and the drain contacts.

[0012] Such transistors are n-channel under positive gate bias and p-channel under negative gate bias. Hole and electron mobilities of respectively < 10⁻⁶ cm²/(Vs) and < 10⁻⁷ cm²/(Vs) were reported for P3HT doped with a 25 mol % concentration of BPPC.

[0013] Balberg describes in Applied Physical Letters , Vol.79, No.2, 9 July 2001 a fullerene (C₆₀)-polymer network composite structure showing as well a connectivity of a percolative C₆₀ network and additionally a connectivity of the polymer network. The effectivity of the coupling phenomenon and the recombination kinetics depends on the molar fractions of both components.

[0014] Brown discloses in U.S.Patent 5,629,530 a solid state mixture of organic donor and acceptor molecules different from the present invention and encounters some difficulties to obtain good balanced mobility values for holes and electrons transports in his organic donor/acceptor layer.

[0015] Dodabalapur discloses in U.S.Patent 5,596,208 organic field-effect transistors based on superposed evaporated organic n-type and p-type layers. In this patent evaporated C₆₀ molecules are used as electron transporters in combination with Cr/Au bottom contacts. This technology leads to high production costs and this is a major disadvantage for organic transistors which are especially developed for low cost applications.

[0016] In 2000 Geens *et. al* in CP544, Electronic Properties of Novel Materials - Molecular Structures, ed. H. Kuzmany *et al.*, Americal Institute of Physics, pages 516-520 reported field effect mobility measurements of organic field-effect transistors using an active region consisting of a conjugated polymer poly(2-methoxy-5-(3',7'-dimethyloctyloxy)-1,4-phenylene-vinylene (MD-MO-PPV) blended with a soluble derivative of C₆₀ (6,6)-phenyl C₆₁-butyric acid methyl ester (PCBM) in ratios of 1:2, 1:3, 1:4 and 1:10 with gold source and drain electrode, p-conducting silicon as the gate electrode and a silica insulating layer.

[0017] Geens *et al.* did not report any transistor with ambipolar properties. In 2000 Brabec *et al.* in Synthetic Metals, volume 121, pages 1517-1520, reported bulk heterojunction photovoltaic devices based on a spin-coated blend of MDVO-PPV and PCBM. Devices with power efficiencies higher than 2.5% under AM1.5 were reported.

Aims of the Invention

[0018] The present invention aims to provide a network of organic molecules providing high balanced mobility values for holes and electrons combined to single

top contacts for low-cost large scale organic ambipolar transistor productions.

Summary of the invention

[0019] It has been surprisingly found that organic field-effect transistors using known blends of OC₁C₁₀-PPV and PCBM, which exhibited high hole mobilities with Au source and drain electrodes, become ambipolar upon using source and drain electrodes comprising Au, LiF and Al exhibiting hole field-effect mobilities of 2×10^{-5} cm²/Vs and electron field effect mobilities of 2×10^{-4} cm²/Vs as determined from the saturation regime. These field effect mobilities were surprisingly found to be comparable with hole and electron field-effect mobilities exhibited by organic field-effect transistors with Au source and drain electrodes (1×10^{-3} cm²/Vs) and LiF/Al source and drain electrodes (3×10^{-4} cm²/Vs) respectively as described in Figure 7.

[0020] The present invention discloses a thin film ambipolar organic metal oxide semiconductor field-effect transistor (MOSFET) comprising source and drain contacts and an organic semiconductor layer, said layer comprises hole and electron transporter, characterised in that said electron transporter comprises (6,6)-phenyl C₆₁-butyric acid methyl ester (PCBM) and said hole transporter comprises poly(2-methoxy-5-(3',7'-dimethyloctyloxy)-1,4-phenylene-vinylene) (OC₁C₁₀-PPV) and/or poly(3-hexylthiophene)(P3HT)

[0021] In a particular aspect of the invention the organic semiconductor layer comprises a blend of OC₁C₁₀-PPV : PCBM in weight proportions of (1:4).

[0022] A preferred aspect of the present invention is that the organic OC₁C₁₀-PPV : PCBM mix is dissolved in chlorobenzene or in 1,2-dichlorobenzene or xylene.

[0023] A preferred aspect of the present invention is that source and/or drain electrodes comprise LiF and Al and/or Au.

[0024] In a further preferred embodiment of the present invention the source and drain contacts are a codeposited (< 1 nm thick) Au/LiF layer in weight ratios in the range 10:1 to 1:10, followed by the deposition of an Al layer.

Short description of the drawings

[0025] Fig.1 represents a schematic device structure of the thin film transistor with top source and drain electrodes (1), an organic semiconductor layer (2), an insulation layer (3) and a gate contact (4).

[0026] Fig.2 represents the molecule used as electron transporter. This is a modified soluble fullerene for spin coating applications, (6,6)-phenyl C₆₁-butyric acid methyl ester, (PCBM).

[0027] Fig.3 represents the molecule used as hole transporter poly(2-methoxy-5-(3',7'-dimethyloctyloxy)-1,4-phenylene-vinylene), (OC₁C₁₀-PPV).

[0028] Fig.4 represents a schematic view of the as-

sembling of the molecules in the organic layer, building some electron and hole transport channels.

[0029] Fig.5 represents the p-channel electrical characteristics of a field-effect transistor with an active layer of a (1:4) weight ratio blend of OC₁C₁₀-PPV : PCBM and Au top contacts.

[0030] Fig.6a represents the n-channel electrical characteristics of a field-effect transistor with an active layer of a (1:4) weight ratio blend of OC₁C₁₀-PPV : PCBM and Al top contacts.

[0031] Fig.6b represents the improved n-channel electrical characteristics of a field-effect transistor with an active layer of a (1:4) weight ratio blend of OC₁C₁₀-PPV : PCBM with LiF/Al top contacts. A thin layer of LiF was evaporated on top of the organic blend prior to the deposition of the Al electrode.

[0032] Fig.7 represents the hole and electron mobilities in OC₁C₁₀-PPV : PCBM (1:4) weight ratio as determined from devices with respectively Au top contacts and LiF/Al top contacts.

[0033] Fig.8 represents the p-channel and n-channel electrical characteristics of a field-effect transistor with an active layer of a (1:4) weight ratio blend of OC₁C₁₀-PPV : PCBM with Au:LiF/Al top contacts. A thin layer of co-evaporated Au and LiF (represented as (Au: LiF)) in a (1:1) weight ratio was formed on top of the organic blend prior to the deposition of the Al electrode.

Detailed description of the invention

[0034] Fig.1 shows the set-up of the organic field-effect transistor and helps [to understand] the experimental conditions of the different steps to be understood. In Figure 1 the gate electrode is a self-supporting piece of doped silicon coated with an insulating silica layer on one side and contacted with aluminium gate contact (4) on the other.

[0035] In a first cleaning step, the SiO₂ surface is properly cleaned by immersing the substrate consecutively in heated acetone and heated IPA (isopropylalcohol).

[0036] In a second step a surface treatment of the SiO₂ is performed by an oxygen plasma during 5 minutes at 200 W. This step is followed by a HMDS (hexamethyldisilazane) treatment.

[0037] This treatment leads to an extremely hydrophobic SiO₂ surface.

[0038] The self-supporting piece of doped silicon coated with an insulating silica layer on one side and contacted with aluminium gate contact (4) on the other is then introduced into a nitrogen box where spin coating of the organic semiconductor layer onto the insulating silica layer can be carried out. The presence of nitrogen keeps oxygen and moisture out of the organic semiconductor layer [film]. Furthermore, the self-supporting piece of doped silicon coated with an insulating silica layer coated on one side and contacted with aluminium gate contact (4) on the other, now coated with the or-

ganic semiconductors layer, can be directly transferred from the nitrogen box to the metal deposition chamber in this way avoiding the formation of an insulating metal oxide layer at the interface between the organic semiconductor layer and the evaporated metal.

[0039] The organic blends are prepared by dissolving OC₁C₁₀-PPV and PCBM in a (1:4) weight ratio in an organic solvent, preferably, water-free chlorobenzene, water-free 1,2-dichlorobenzene or xylene. In practice, 5mg of OC₁C₁₀-PPV and 20mg of PCBM are dissolved in 1ml of one of the above mentioned solvents. A magnetic stirring rod is added to the solution in a vessel and is kept stirring overnight at a temperature of 60 °C. The organic layer is formed by covering the SiO₂ with the solution and spincoat the surface 40s at 1200 rpm, followed by 20s at 4000 rpm. This technique supplies organic layers of suitable thickness (100 to 200 nm). A similar technique can be used for poly(3-hexylthiophene)(P3HT).

[0040] In the next step the top electrodes are evaporated through a shadow mask defining channel lengths ranging between 30 microns and 200 microns and channel widths of 2 mm or 10 mm.

Different types of top contacts can be formed:

- for hole transport: Au(100 nm)
- for electron transport: LiF(0.6 nm)/Al(120 nm)
- for electron and hole transport: Au:LiF (0.6 nm)/Al (120 nm)

[0041] In latter case first a very thin layer is formed by the co-evaporation of Au and LiF at an extremely low deposition rate, typically 0.1 Angstrom/sec or 10pm/sec for Au as well as for LiF. In this way a mixed layer of 0.6 nm Au:LiF could be formed in 30 seconds. This step is followed by the deposition of an Al layer of approximately 120 nm thickness.

[0042] The work function of Au is such that it forms an ohmic contact with the highest occupied molecular orbital (HOMO) of OC₁C₁₀-PPV, resulting in a low injection barrier for the holes.

[0043] A low injection barrier for the electrons into the lowest unoccupied (LUMO) of PCBM was realised by depositing LiF/Al drain and source contact electrodes onto the organic blend. The co-deposition of Au and LiF, followed by the deposition of Al was surprisingly found to combine these two properties.

[0044] After the set-up of the different functional layers on the organic field-effect transistor, the samples were taken out of the deposition chamber and measured in a flow of nitrogen.

[0045] FET characterisation was performed using a HP4156A analyser, with the source contact connected to ground.

[0046] The measurement mode of the FET is determined by the gate voltage, which induces an accumulation layer of charges in the region of the conduction channel adjacent to the interface with the SiO₂.

[0047] For p-channel (n-channel) operation, a negative (positive) gate voltage is applied to induce an accumulation layer of holes (electrons), allowing the measurement of the hole (electron) mobility. The field-effect mobilities μ_{FE} were calculated from the saturation regime of the drain-source current (I_{ds}) using the formula:

$$I_{ds,sat} = \frac{\mu_{FE} W C_{ox}}{2L} (V_{gs} - V_t)^2$$

where W and L are respectively the conduction channel width and length, C_{ox} is the capacitance of the insulating SiO₂ layer, V_{gs} is the gate voltage, and V_t is the threshold voltage.

[0048] We have determined the following mobilities:

[0049] For the hole mobility, Au contacts → $\mu_h = 1 \times 10^{-3} \text{ cm}^2/\text{Vs}$ (Fig. 5)

[0050] For the electron mobility, LiF/Al contacts → $\mu_e = 3 \times 10^{-4} \text{ cm}^2/\text{Vs}$ (Fig. 6)

[0051] The previous mobility values are summarised in Fig. 7 with their respective error bars.

[0052] For the hole and electron mobility at the same time, Au:LiF/Al with a (1:1) ratio for (Au:LiF) → $\mu_h = 2 \times 10^{-5} \text{ cm}^2/\text{Vs}$ and $\mu_e = 2 \times 10^{-4} \text{ cm}^2/\text{Vs}$ (Fig. 8).

Claims

- 30 1. An organic field-effect transistor comprising separate source and drain electrodes independent of one another on a layer configuration, said layer configuration comprising at least an organic semiconductor layer constituting a channel between said source and drain electrodes, an insulation layer underlying said organic semiconductor layer and a gate electrode formed on the opposite side of the insulation layer, wherein said organic semiconductor layer comprises hole and electron transporters, said electron transporters comprising (6,6)-phenyl C₆₁-butyric acid methyl ester (PCBM) and said hole transporters comprising poly(2-methoxy-5-(3',7'-dimethyloctyloxy)-1,4-phenylene-vinylene) (OC₁C₁₀-PPV) and/or poly(3-hexylthiophene) (P3HT), wherein said source and drain electrodes independently comprise LiF and Al and/or Au.
2. Organic field-effect transistor according to claim 1, wherein said source electrode and/or said drain electrode comprises Au and LiF in a weight ratio in the range of 1:10 to 10:1.
3. Organic field-effect transistor according to claim 2, wherein said source electrode and/or said drain electrode comprises Au and LiF in a weight ratio in the range of 1:1 to 4:1.
4. Organic field-effect transistor according to claim 1,

- wherein said source electrode and/or said drain electrode comprises codeposited Au and LiF.
5. Organic field-effect transistor according to claim 4, wherein an Al layer is deposited on top of said codeposited Au/LiF layer. 5
6. Organic field-effect transistor according to claim 1, wherein said source electrode and/or said drain electrode comprises an LiF/Al contact to said organic semiconductor layer. 10
7. Organic field-effect transistor according to claim 1, wherein said organic semiconductor layer comprises a blend of OC₁C₁₀-PPV : PCBM in a weight ratio in the range 0.5:1 to 5:1. 15
8. Organic field-effect transistor according to claim 1, wherein said organic semiconductor layer comprises traces of an organic solvent. 20
9. Organic field-effect transistor according to claim 8, wherein said organic semiconductor layer comprises traces of at least one organic solvent selected from the group consisting of chlorobenzene, 1,2 dichlorobenzene and xylene. 25
10. A process for preparing an organic field effect transistor comprising the following steps: 30
- providing a gate electrode or forming a gate electrode on a substrate,
 - depositing on said gate electrode an insulating layer preferably a SiO₂ layer,
 - depositing on said insulating layer an organic semiconductor layer comprising a mixture of at least two components, the first component being (6,6)-phenyl C₆₁-butyric acid methyl ester and the second component being poly(2-methoxy-5-(3',7'-dimethyloctyloxy)-1,4-phenylene-vinylene) (OC₁C₁₀-PPV) and/or poly(3-hexylthiophene) (P3HT), and 35
 - creating a source and a drain electrodes independently of one another, both said source and drain electrodes comprising LiF and Al and/or Au. 40
 - creating a source and a drain electrodes independently of one another, both said source and drain electrodes comprising LiF and Al and/or Au. 45
11. Process according to claim 10, wherein said creation of said source and drain electrodes is performed by codeposition of Au and LiF in a weight ratio in the range of 1:10 to 10:1. 50
12. Process according to claim 11, wherein said codeposition of Au and LiF is followed by the deposition of an Al layer. 55
13. Process according to claim 10, wherein said organic semiconductor layer is deposited from a solution or dispersion in one or more organic solvents.
14. Process according to claim 10, wherein said organic semiconductor layer is deposited by spray-drying.

Au or LiF/Al or LiF:Au/Al top contacts
(source and drain electrodes) (1)

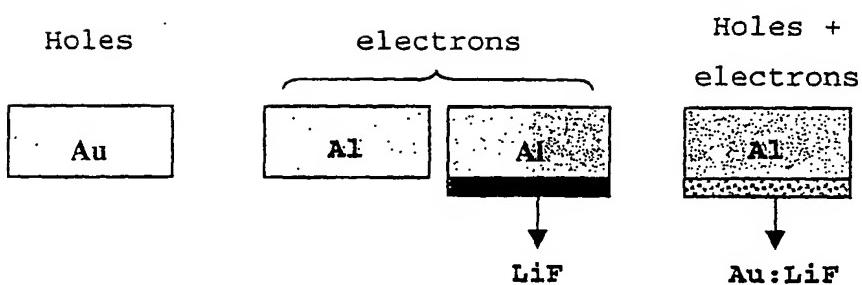
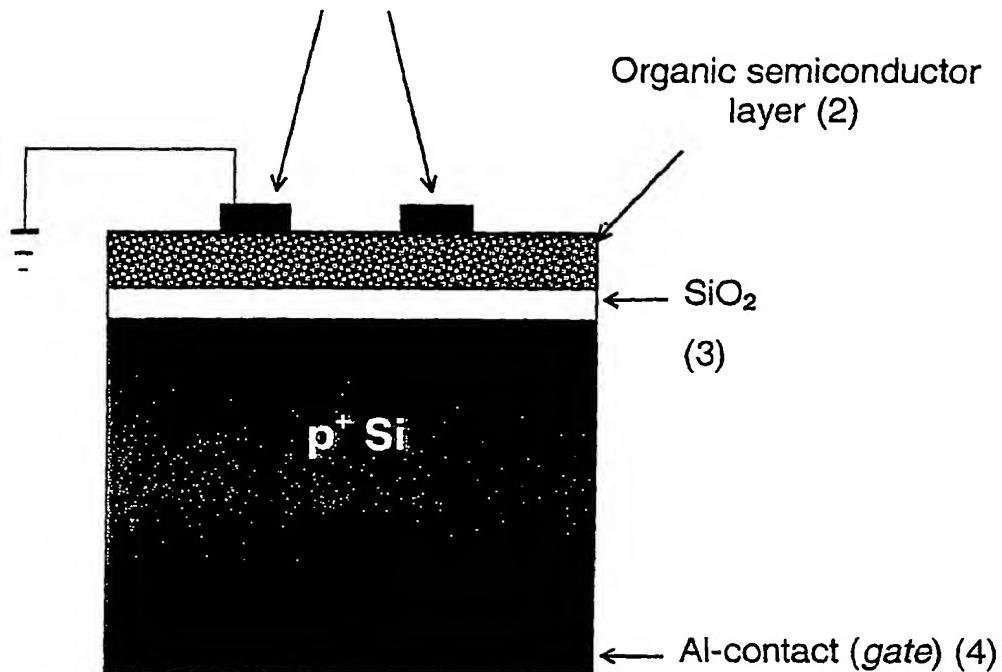


Fig.1

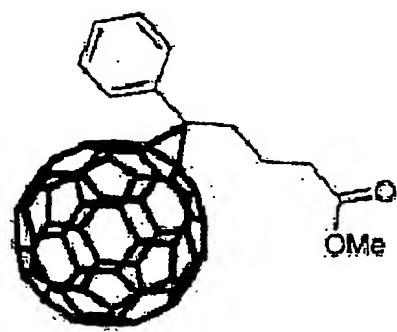


Fig. 2

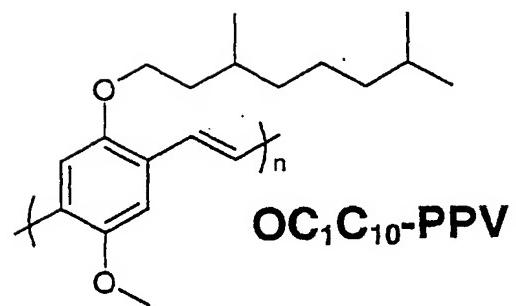


Fig. 3

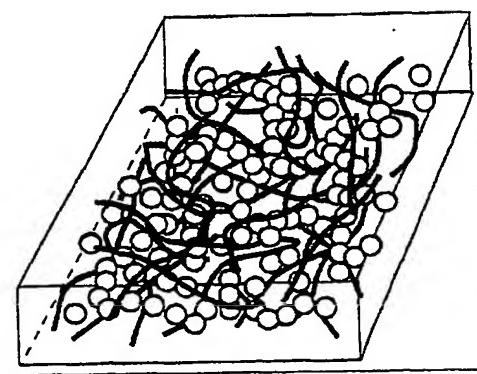


Fig. 4

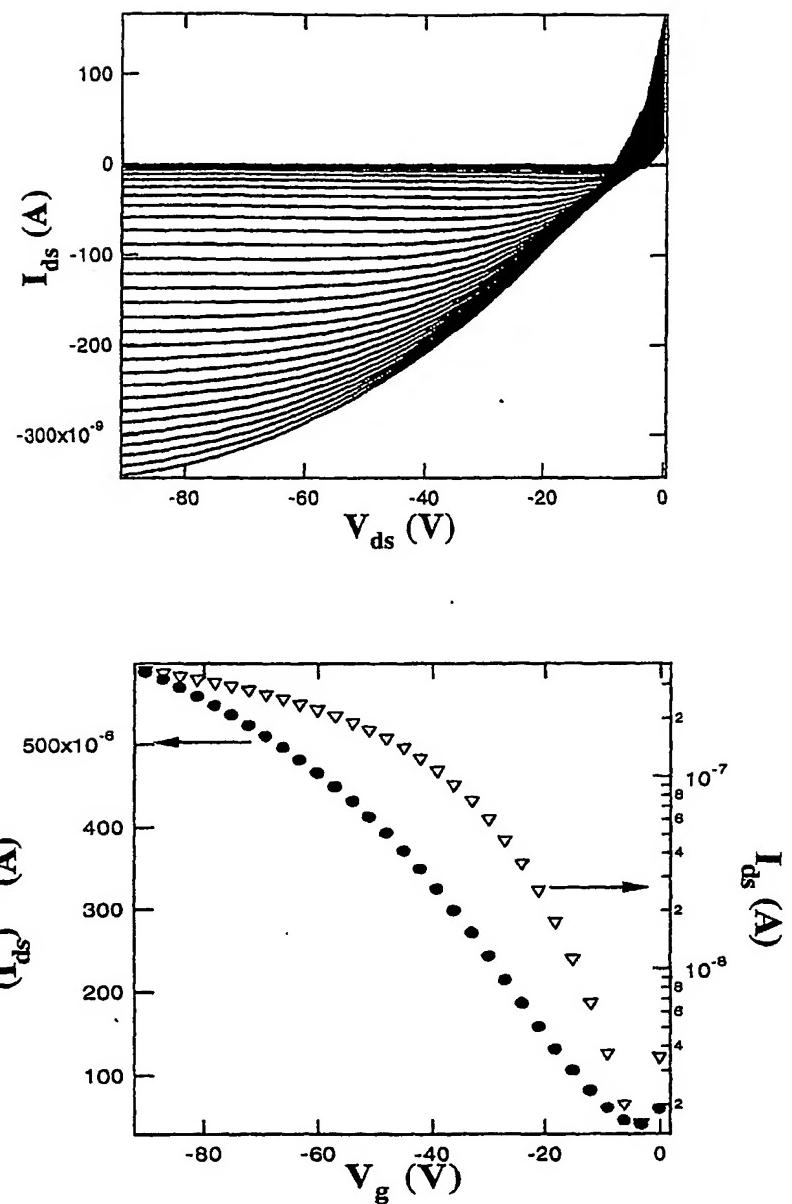


Fig. 5

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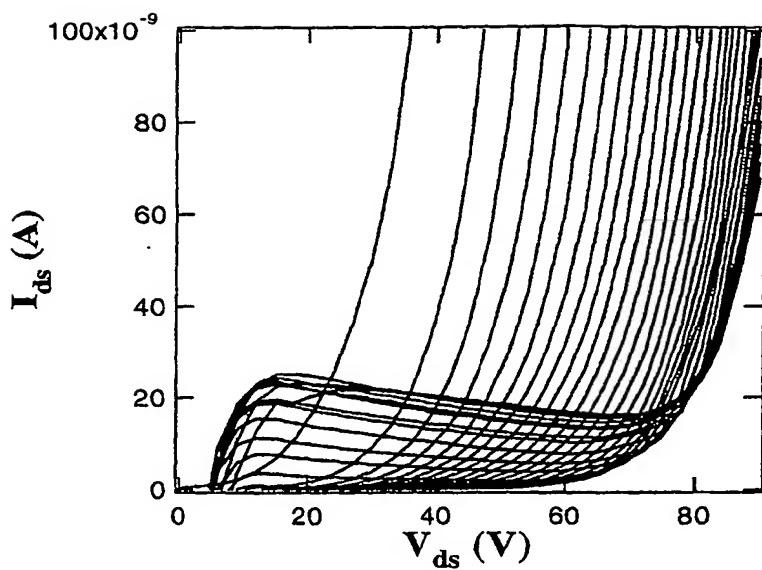


Fig. 6a

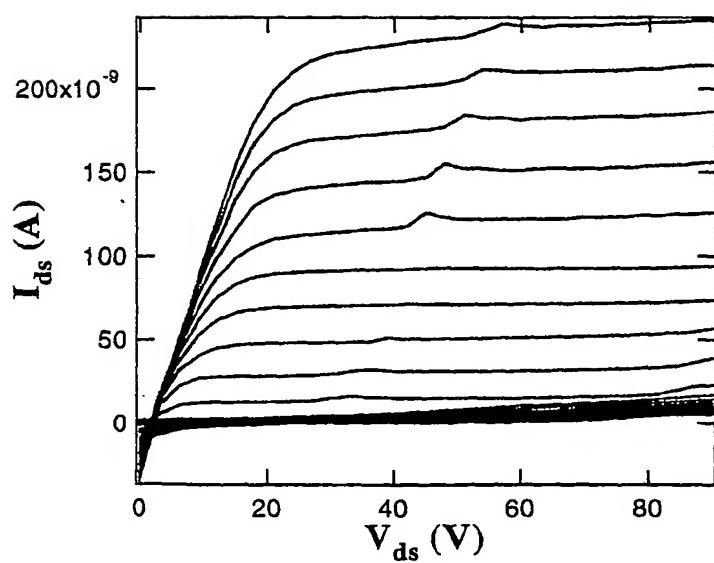


Fig. 6b

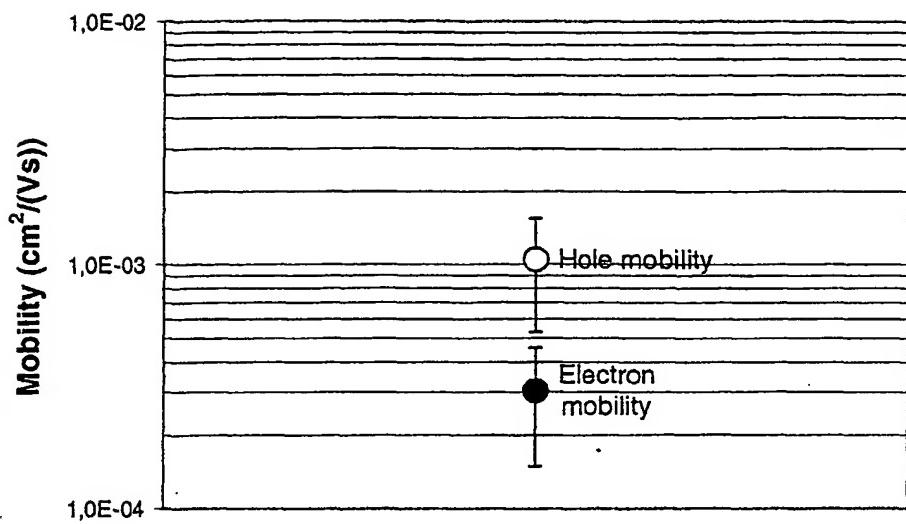


Fig. 7

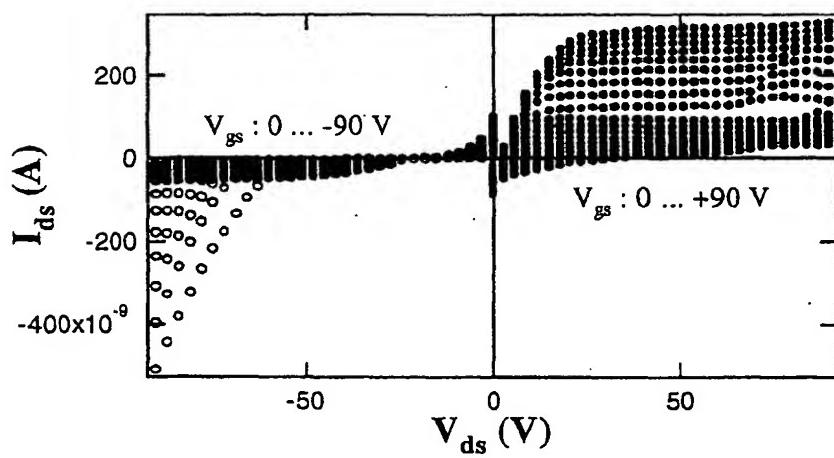


Fig. 8



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EUROPEAN SEARCH REPORT

Application Number
EP 02 44 7197

DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.7)						
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim							
D,Y	GEENS W ET AL: "Field-effect mobility measurements of conjugated polymer/fullerene photovoltaic blends" ELECTRONIC PROPERTIES OF NOVEL MATERIALS - MOLECULAR NANOSTRUCTURES. 14TH INTERNATIONAL WINTERSCHOOL/EUROCONFERENCE, KIRCHBERG, AUSTRIA, 4-11 MARCH 2000, no. 544, pages 516-520, XP008001919 AIP Conference Proceedings, 2000, AIP, USA ISSN: 0094-243X * the whole document * -----	1	H01L51/20						
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D,A	EP 0 716 459 A (AT & T CORP) 12 June 1996 (1996-06-12) * abstract; figure 2 * -----	1							
<p>The present search report has been drawn up for all claims</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 33%;">Place of search</td> <td style="width: 33%;">Date of completion of the search</td> <td style="width: 34%;">Examiner</td> </tr> <tr> <td>THE HAGUE</td> <td>21 February 2003</td> <td>Königstein, C</td> </tr> </table> <p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>				Place of search	Date of completion of the search	Examiner	THE HAGUE	21 February 2003	Königstein, C
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ON EUROPEAN PATENT APPLICATION NO.

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21-02-2003

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